



UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Shunpei YAMAZAKI et al.
Serial No. 09/837,877
Filed: April 19, 2001
For: SEMICONDUCTOR DEVICE

Examiner: M. Lewis
Group Art Unit: 2822

#17/105
Actual
10/9/03

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure as set forth in 37 C.F.R. §1.56, Applicants hereby submit the following information in conformance with 37 C.F.R. §§ 1.97 and 1.98. Pursuant to 37 C.F.R. § 1.98, a copy of each of the documents cited is enclosed.

Please note that U.S. Patent Nos. 6479333 and 6603453 were recently cited by the Examiner in related pending application Serial No. 09/837,558.

A check in the amount of \$180 is being submitted to comply with the provisions of 37 C.F.R. § 1.97(c)

It is requested that the accompanying PTO-1449 be considered and made of record in the above-identified application. To assist the Examiner, the documents are listed on the attached form PTO-1449. It is respectfully requested that an Examiner initial a copy of this form be returned to the undersigned.

The Commissioner is hereby authorized to charge any fees connected with this filing which may be required now, or credit any overpayment to Deposit Account No. 19-2380 (740756-2298).

Respectfully submitted,

By:

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